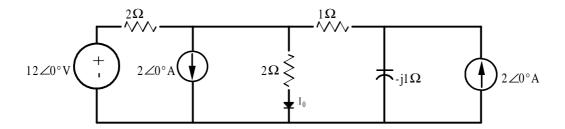
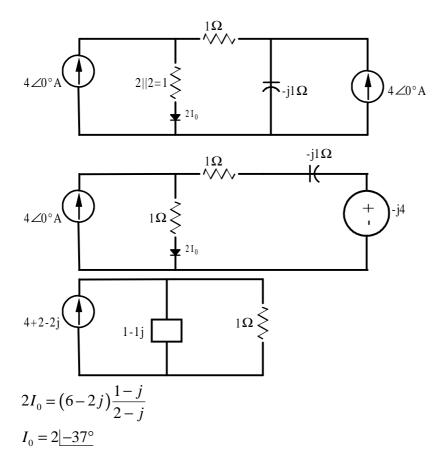
Problem 7.61

Use source exchange to find the current I_0 in the network shown.

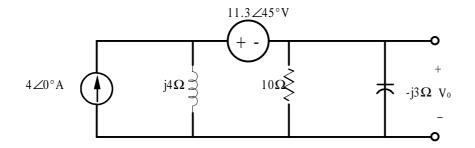


Suggested Solution

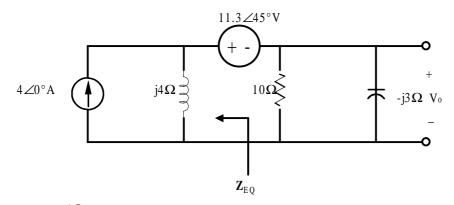


Problem 7.70

Find V_x in the circuit shown using Norton's theorem.



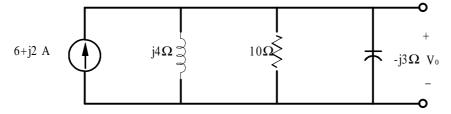
Suggested Solution



$$Z_{EQ} = j4\Omega$$

$$4 = \frac{V}{j4} + I_{SC} \quad where \quad V = 11.3 \boxed{45}^{\circ}V$$

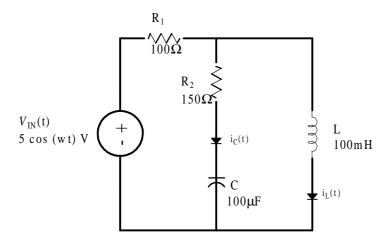
$$I_{SC} = 4 + \frac{jV}{4} = 6 + j2A$$



$$V_X = I_{SC} \left[10 \parallel j4 \parallel - j3 \right] = I_{SC} \left[\frac{1}{\frac{1}{10} + \frac{1}{j4} + \frac{1}{-j3}} \right] = \frac{I_{SC}}{0.10 + j0.08} = 48.59 \underline{|-21.37^{\circ}V}$$

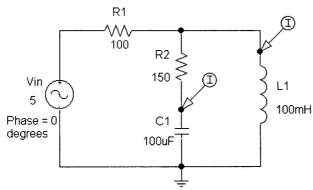
Problem 7.76

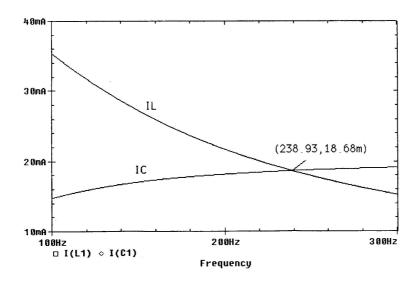
Using the PSPICE Schematics editor, draw the circuit shown. At what frequency are the magnitudes of $i_C(t)$ and $i_L(t)$ equal?



Suggested Solution

This *Schematics* circuit was simulated over the frequency range 100 Hz to 300 Hz. Since current into pin markers were placed in the circuit, PROBE will automatically plot the required current magnitudes.





PROBE results show that the voltage and current phases are roughly equal at 238.9 Hz