Engin 103 Fall '06 Meeting #18: April 5, 2007

Circuit Analysis with LabVIEW IV: the equations for wiring in the Block Diagram are shown in the link to this lab in the e-syllabus. After completing Circuit Analysis with LabVIEW III, you have noticed, as shown in the figure below, that there are repeating operations that can be performed by a sub VI.



In fact, in the following equation to produce the total current I:

$$I = \frac{V}{R1 + R2 \| \{R3 + [R4\| (R5 + R6)]\}} = \frac{V}{R1 + \frac{R2 \cdot \{R3 + \frac{R4 \cdot (R5 + R6)}{R4 + R5 + R6}\}}{R2 + R3 + \frac{R4 \cdot (R5 + R6)}{R4 + R5 + R6}}$$

in the first expression, the parallel "||" operation has been used to replace a repeating group of operations (a product is divided by the sum of a same two variables). This "||" operation will be performed by the "parallel" sub VI.

In these other equations to produce V2 toV4:

$$V2 = V - I \cdot R1$$

$$V4 = V2 - \left(I - \frac{V2}{R2}\right) \cdot R3$$

$$V6 = V4 - \left(I - \frac{V2}{R2} - \frac{V4}{R4}\right) \cdot R5 = \left(I - \frac{V2}{R2} - \frac{V4}{R4}\right) \cdot R6$$

there is a repeating pattern of V-I*R, where I is replaced by the quantity within parentheses in the last two equations. This operation will be performed by the "V_next" sub VI. Also there is a pattern in both parentheses: I-V/R, which will be performed by the "I_after" sub VI.







To call in a subVI you created to perform a group of operations

(e.g. the Parallel subVI), right-click within the Block Diagram, All Functions/Select a VI. Then wire its terminals (defined during the process of assigning connectors) to the rest of the circuit.



For Circuit III with subVI's, repeat the same steps to insert the Vnext subVI: right-click within the Block Diagram, All Functions/Select a VI. Then wire its terminals (defined during the process of assigning connectors) to the rest of the circuit.





The Block Diagram for Circuit III when the three subVI's (Parallel, Vnext, I_after) are used contains only 11 operators now. This illustrates the purpose of using subVI's (or subroutines), that is, to simplify the reading of a code, it is easier to pinpoint and fix an error. Also, in a graphical programming language, such as LabVIEW, the interconnections between a subroutine and other elements in the code are easy to see.



Suggested items to write in the Engin 103 logbook:

1) How many icons in the Block Diagram did you use to implement equation (1) for the total current I in the Circuit Analysis with LabVIEW III link with and without the parallel sub VI ? Repeat the same for equations (2), (3), and (4) to obtain V2, V4, V6

2) Explain in your own words the advantages and disadvantages of using sub VI's